

LOW POWER LOW VOLTAGE TRANSISTOR-TRANSISTOR LOGIC I/O DRIVER

Abstract of the Disclosure

An I/O driver comprising: a circuit adapted to be powered by a first power supply. The circuit is adapted to receive a first signal referenced to the voltage of a second power supply and is adapted to convert the first signal to a second signal of the same logical value as the first signal and referenced to the voltage of the first power supply. The circuit is adapted to maintain the second signal on an output of the I/O driver when the second power supply is powered off.

Figures